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PATENT
Attorney Docket No.: 016747-009910US
Client Reference No.: P2404/MCR

Assistant Commissioner for Patents
Washington, D.C. 20231

On

Aug 30, 2001

TOWNSEND and TOWNSEND and CREW LLP

By

Kevin L. McNeill

Technology Center 2100

OCT 19 2001

RECEIVED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ashley Saulsbury, et al.

Application No.: 09/802,017

Filed: March 8, 2001

For: VLIW Computer Processing
Architecture with On-chip DRAM Usable
as Physical Memory or Cache Memory

Examiner: Unassigned

Art Unit: 2183

INFORMATION DISCLOSURE
STATEMENT UNDER 37 CFR §1.97 and
§1.98

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

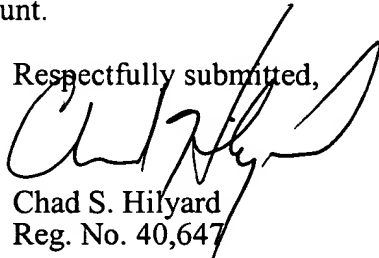
The references cited on attached form PTO-1449 are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and

no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



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